In the Claims

1

3

4 5

6 7

8

9

10

11

12

13

14 15

16

17

18 19

20

1

2

- (Currently Amended) A method for measuring a sense node voltage associated with a light-detecting element, the sense node voltage being related to light intensity incident upon the light-detecting element, comprising:
- (a) generating a first integration reset pulse to enable a resetting of the sense node voltage to a voltage value substantially equal to a reset voltage value associated with the first integration reset pulse, an edge of the first integration reset pulse triggering a beginning of a first integration period;
- (b) generating a second integration reset pulse to enable a resetting of the sense node voltage to a voltage value substantially equal to a reset voltage value associated with the second integration reset pulse, an edge of the second integration reset pulse triggering a beginning of a second integration period;
- (c) generating, subsequent to the generation of the first integration reset pulse and prior to the generation of the second integration reset pulse, a plurality of intra-period reset pulses to enable resetting of the sense node voltage to a plurality of voltage values, each voltage value being substantially equal to a reset voltage value associated with the generated intra-period reset pulse, the intra-period reset pulses progressively decreasing in pulse width; and
- (d) measuring, only once during an integration period, the sense node voltage generated in response to incident light intensity, the sense node voltage being measured subsequent to the generation of the plurality of intra-period reset pulses and prior to the generation of the second integration reset pulse.
- (Original) The method as claimed in claim 1, wherein the reset voltage values associated with the plurality of intra-period reset pulses are less than the reset voltage value associated with the first integration reset pulse.

Patent Application Number: 10/751,562 Attorney Docket Number: GC.7027

intra-period reset pulses is non-periodic. 5. (Original) The method as claimed in claim 2, wherein the generation of the plurality of 1 intra-period reset pulses is non-periodic. 2 1 6. (Original) The method as claimed in claim 3, wherein the generation of the plurality of 2 intra-period reset pulses is non-periodic. 7. (Original) The method as claimed in claim 1, wherein the generation of the plurality of 1 2 intra-period reset pulses is periodic. 8. (Original) The method as claimed in claim 2, wherein the generation of the plurality of 1 2 intra-period reset pulses is periodic. 1 9. (Original) The method as claimed in claim 3, wherein the generation of the plurality of intra-period reset pulses is periodic. 2 10. (Original) The method as claimed in claim 1, further comprising: (e) resetting a voltage level of the sense node when the voltage level of the sense node is 2 3 less than the voltage value associated with the generated intra-period reset pulse. 11. (Currently Amended) A method for measuring a sense node voltage associated with a 1 light detecting element, the sense node voltage being related to light intensity incident upon the 2 3 light detecting element, comprising:

3. (Original) The method as claimed in claim 2, wherein the reset voltage value

4. (Original) The method as claimed in claim 1, wherein the generation of the plurality of

associated with one of the plurality of intra-period reset pulses is less than the reset voltage value

associated with a previous one of the plurality of intra-period reset pulses.

1 2

3

1

Patent Application Number: 10/751,562 Attorney Docket Number: GC.7027

progressively decreasing in pulse width; and (c) measuring, only once during the integration period, the sense node voltage generated in response to incident light intensity, the sense node voltage being measured subsequent to the plural resettings of the voltage level of the sense node and prior to initiating a next integration period. 12. (Original) The method as claimed in claim 11, wherein the voltage levels associated with the plural resettings of the voltage level of the sense node have voltage values less than a voltage value used to reset the voltage level of the sense node at a beginning of the integration period. 13. (Original) The method as claimed in claim 11, wherein a voltage level associated with one of the plural resettings during the integration period is less than a voltage level associated with a previous one of the plural resettings during the integration period. 14. (Original) The method as claimed in claim 11, wherein the plural resettings generate a non-periodic pattern. 15. (Original) The method as claimed in claim 11, wherein the plural resettings generate a periodic pattern. 16. (Currently Amended) A method for capturing a frame of image data associated with a scene using an array of light-detecting elements, each light-detecting element having an associated sense node, comprising: (a) initiating an integration period for the array of light-detecting elements, the

(a) initiating an integration period for the light-detecting element;

(b) resetting, using a plurality of intra-period reset pulses, a plurality of times, the voltage

level of the sense node after initiating the integration period, the intra-period reset pulses

4

5

7

8

9

10

11

1

3

4

1

2

3

1 2

1

1

2

3

4 5

integration period being associated with the frame of image data;

Patent Application Number: 10/751,562 Attorney Docket Number: GC.7027

(b) generating a plurality of intra-period reset pulses during the integration period such that voltage levels of the sense nodes associated with a portion of the array of light-detecting elements are enabled to be set a plurality of times during the integration period, the intra-period reset pulses progressively decreasing in pulse width; and

6

7

8

10

11

12

13 14

1

2

3

1

2

3

1

2

3

1

1

2

- (c) measuring, only once during the integration period, the voltage levels of the sense nodes voltages generated in response to incident light intensities, the sense node voltages being measured subsequent to a final resetting of the voltage levels of the sense nodes associated with the portion of the array of light-detecting elements and prior to initiating a next integration period.
- 17. (Original) The method as claimed in claim 16, wherein the voltage levels associated with the plurality of intra-period reset pulses have voltage values less than a voltage value used to reset the voltage levels of all the sense nodes at a beginning of the integration period.
- 18. (Original) The method as claimed in claim 16, wherein a voltage level associated with the plurality of intra-period reset pulses during the integration period is less than a voltage level associated with a previous one of the plurality of reset pulses during the integration period.
- 19. (Original) The method as claimed in claim 16, wherein the voltages levels associated with the plurality of intra-period reset pulses during the integration period progressively decrease during the integration period.
- 20. (Original) The method as claimed in claim 16, further comprising:
- (d) resetting a voltage level of the sense node when the voltage level of the sense node is
 less than the voltage value associated with the generated reset pulse.
 - 21. (Currently Amended) A method for measuring a sense node voltage associated with a light detecting element, the sense node voltage being related to light intensity incident upon the light detecting element, comprising:

(a) initiating an integration period for the light-detecting element;

4

5 6

7

9

10

11

1

2

1

2

3

4

1

2

3

5

6 7

8

9

- (b) resetting, a first number of times during the integration period, <u>using a number of intra-period reset pulses</u>, the number of intra-period reset pulses being equal to the first number of times, the voltage level of the sense node after initiating the integration period, the intra-period reset pulses progressively decreasing in pulse width; and
- (c) measuring the sense node voltage generated in response to incident light intensity, the sense node voltage being measured a second number of times during the integration period, the second number of times being less than the first number of times.
- 22. (Original) The method as claimed in claim 21, wherein the voltage level associated with resetting of the voltage level of the sense node has a voltage value less than a voltage value used to reset the voltage level of the sense node at a beginning of the integration period.
- 23. (Original) The method as claimed in claim 21, wherein the first number of times is greater than one and wherein a voltage level associated with one of the plural resettings during the integration period is less than a voltage level associated with a previous one of the plural resettings during the integration period.
- 24. (Currently Amended) A method for measuring a sense node voltage associated with a light-detecting element, the sense node voltage being related to light intensity incident upon the light-detecting element, the light-detecting element having a reset switch associated therewith so as to set a voltage level of the sense node, comprising:
- (a) generating a first integration reset pulse to enable a resetting of the sense node voltage to a voltage value substantially equal to a reset voltage value associated with the first integration reset pulse, an edge of the first integration reset pulse triggering a beginning of a first integration period;
- (b) generating a second integration reset pulse to enable a resetting of the sense node voltage to a voltage value substantially equal to a reset voltage value associated with the second

integration reset pulse, an edge of the second integration reset pulse triggering a beginning of a second integration period;

- (c) generating, subsequent to the generation of the first integration reset pulse and prior to the generation of the second integration reset pulse, a train of progressively decreasing intraperiod reset pulses to enable resetting of the sense node voltage to a plurality of voltage values, each voltage value being substantially equal to a reset voltage value associated with the generated intra-period reset pulse; and
- (d) measuring, only once during an integration period, the sense node voltage generated in response to incident light intensity, the sense node voltage being measured subsequent to the generation of the train of progressively decreasing intra-period reset pulses and prior to the generation of the second integration reset pulse, the train of progressively decreasing intra-period reset pulses progressively decreasing in pulse width.

Claim 25 (Cancelled)

11

12

13 14

15

16

17

18 19

20

21

22

1

1

1

2

2

3

26. (Original) The method as claimed in claim 24, wherein the train of progressively decreasing intra-period reset pulses progressively decreases in voltage level.

Claim 27 (Cancelled)

- 28. (Original) The method as claimed in claim 24, wherein the train of progressively decreasing intra-period reset pulses represents a non-periodic pattern.
- 1 29. (Original) The method as claimed in claim 24, wherein the train of progressively decreasing intra-period reset pulses represents a periodic pattern.
 - 30. (Currently Amended) The method as claimed in claim 24, further comprising:
 - (e) resetting a voltage level of the sense node when the voltage level of the ef-the sense node is less than the voltage value associated with the generated intra-period reset pulse.

- 31. (Currently Amended) A method for measuring a sense node voltage associated with a light-detecting element, the sense node voltage being related to light intensity incident upon the light-detecting element, comprising:

 (a) generating a first integration reset pulse to enable a resetting of the sense node
 - (a) generating a first integration reset pulse to enable a resetting of the sense node voltage to a voltage value substantially equal to a reset voltage value associated with the first integration reset pulse, an edge of the first integration reset pulse triggering a beginning of a first integration period;

5

6 7

8

9

10 11

12

13

14

15 16

17

18

19

20

1

2

3

1

2

- (b) generating a second integration reset pulse to enable a resetting of the sense node voltage to a voltage value substantially equal to a reset voltage value associated with the second integration reset pulse, an edge of the second integration reset pulse triggering a beginning of a second integration period;
- (c) generating, subsequent to the generation of the first integration reset pulse and prior to the generation of the second integration reset pulse, a plurality of intra-period reset pulses to selectively reset the sense node voltage to a plurality of voltage values, each voltage value being substantially equal to a reset voltage value associated with the generated intra-period reset pulse, the intra-period reset pulses progressively decreasing in pulse width; and
- (d) measuring, only once during an integration period, the sense node voltage generated in response to incident light intensity, the sense node voltage being measured subsequent to the generation of the plurality of intra-period reset pulses and prior to the generation of the second integration reset pulse.
- 32. (Original) The method as claimed in claim 31, wherein the reset voltage values associated with the plurality of intra-period reset pulses are less than the reset voltage value associated with the first integration reset pulse.
- 33. (Original) The method as claimed in claim 32, wherein the reset voltage value associated with one of the plurality of intra-period reset pulses is less than the reset voltage value associated with a previous one of the plurality of intra-period reset pulses.

Patent Application Number: 10/751,562

- Attorney Docket Number: GC.7027 1 34. (Original) The method as claimed in claim 31, wherein the generation of the plurality 2 of intra-period reset pulses is non-periodic. 1 35. (Original) The method as claimed in claim 32, wherein the generation of the plurality 2 of intra-period reset pulses is non-periodic. 36. (Original) The method as claimed in claim 33, wherein the generation of the plurality 1 of intra-period reset pulses is non-periodic. 2
- 37. (Original) The method as claimed in claim 31, wherein the generation of the plurality 1 2 of intra-period reset pulses is periodic.
 - 38. (Original) The method as claimed in claim 32, wherein the generation of the plurality of intra-period reset pulses is periodic.

1

2

1

2

- 39. (Original) The method as claimed in claim 33, wherein the generation of the plurality 1 2 of intra-period reset pulses is periodic.
 - 40. (Original) The method as claimed in claim 31, wherein the voltage level of the sense node is reset when the voltage level of the sense node is less than the voltage value associated with the generated intra-period reset pulse.